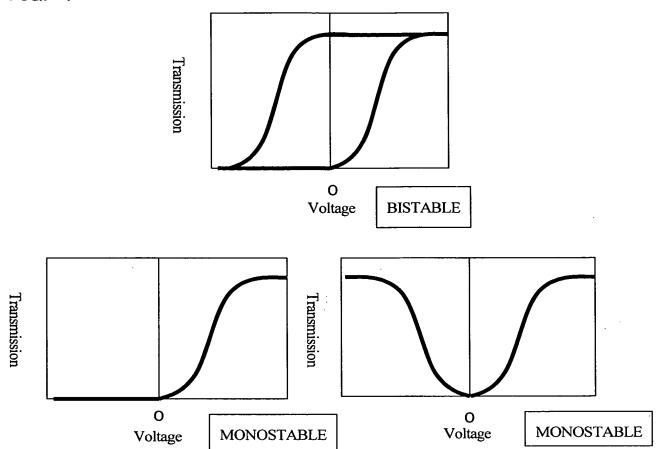
FIG. 1



SMC PHASE

FIG. 3

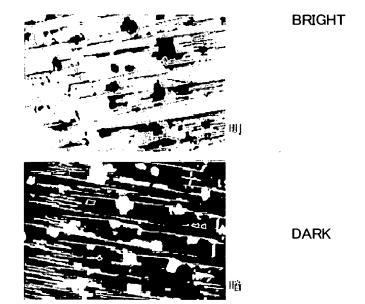


FIG. 4

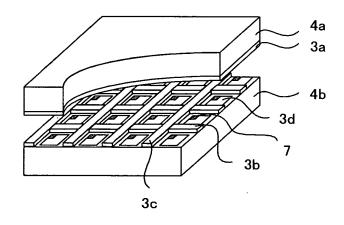


FIG. 5

